L Number	Hits	Search Text	DB	Time stamp
-	19	(motherboard and computer and (cpu microprocessor	USPAT;	2003/04/17 21:20
		processor) and dsp and memory) and ((spoken voice speech)	US-PGPUB;	
		adj1 command)	EPO; JPO;	
			DERWENT;	ļ
į			IBM_TDB	
-!	33	(motherboard and computer and (cpu microprocessor	USPAT;	2003/04/17 21:20
		processor) and dsp and memory) and ((spoken voice speech	US-PGPUB;	
		audio) adj1 command)	EPO; JPO;	
		, , , , , , , , , , , , , , , , , , , ,	DERWENT;	
			IBM TDB	
-	16	(motherboard and computer and (cpu microprocessor	USPAT:	2003/04/17 21:21
į		processor) and dsp and memory) and ((spoken voice speech	US-PGPUB:	
		audio) adj1 command) and pci	EPO; JPO;	,
ļ		,, , , ,	DERWENT;	
			IBM_TDB	
-	13	(motherboard and computer and (cpu microprocessor	USPAT;	2003/04/17 21:24
	, •	processor) and dsp and memory) and ((spoken voice speech	US-PGPUB;	
		audio) adj1 command) and pci and bus and power and input	EPO; JPO;	
		addies adj i command) and per and bus and perfect and impac	DERWENT:	
			IBM_TDB	
_	3	(motherboard and computer and (cpu microprocessor	USPAT;	2003/04/17 21:24
İ	Ū	processor) and dsp and memory) and ((spoken voice speech)	US-PGPUB;	2000/04/1/21:24
		adj1 command) and pci and bus and power and input	EPO; JPO;	
		adji command) and por and bus and power and input	DERWENT;	
			IBM TDB	
	38	(motherboard PCB) and computer and (cpu microprocessor	USPAT;	2003/04/17 21:56
-	30	processor) and dsp and memory and ((spoken speech verbal	US-PGPUB;	2003/04/17 21.30
		voice audio) adj1 command)	EPO; JPO;	
		voice addity adji commandy	DERWENT;	
			IBM_TDB	
_ 1	11	((motherboard PCB) and computer and (cpu microprocessor	USPAT;	2003/04/17 22:00
	• •	processor) and dsp and memory and ((spoken speech verbal	US-PGPUB;	2003/04/17 22.00
		voice audio) adj1 command)) and 704/\$.ccls.	EPO; JPO;	
		Voice addition adjit command)) and 704/\$.cois.	DERWENT;	
ļ			IBM_TDB	
_	10	((motherboard PCB) and computer and (cpu microprocessor	USPAT;	2003/04/17 21:54
-	10	processor) and dsp and memory and ((spoken speech verbal	US-PGPUB;	2003/04/17 21.34
		voice audio) adj1 command)) and 704/\$.ccls. and bus	EPO; JPO;	
	·	Voice addition adjit command/) and 70-74.cols. and bus	DERWENT;	
			IBM_TDB	
	13	((motherboard PCB) and computer and (cpu microprocessor	USPAT;	2003/04/17 21:52
	10	processor) and dsp and memory and ((spoken speech verbal	US-PGPUB;	2003/04/1/ 21.32
		voice audio) adj1 command)) and pci and power	EPO; JPO;	
		Voice addition adjit command), and por and power	DERWENT:	
			IBM TDB	
_	47	(motherboard PCB (main adj1 circuit adj1 board) backplane)	USPAT;	2003/04/17 21:58
-	77	and computer and (cpu microprocessor processor) and dsp	US-PGPUB;	2003/04/1/ 21.30
		and memory and ((spoken speech verbal voice audio) adj1		
		1	EPO; JPO;	
		command)	DERWENT;	
	50	((mather board mather board (mather board) DCD (main adid	IBM_TDB	0000/04/40 46:00
-	50	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:02
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
	4	704/\$.ccls.	IBM_TDB	2002/04/47 20:00
-	1	[(USPAT;	2003/04/17 22:02
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
Ì		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
1	± .	704/\$.ccls. and pci	IBM_TDB	
-	24	, , , , , , , , , , , , , , , , , , ,	USPAT;	2003/04/17 22:10
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
ļ		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and pci	DERWENT;	
		and (memory same dsp)	IBM_TDB	<u> </u>

-	20	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 20:54
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and pci	DERWENT;	
		and (memory same dsp) and power	IBM_TDB	
-	32	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/17 22:11
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
1		704/\$.ccls.) and (memory same dsp)	IBM TDB	
-	23	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/17 22:19
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB:	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT:	
		704/\$.ccls.) and bus and (memory same dsp) and power	IBM TDB	
_	1	"05844824"	USPAT;	2003/04/17 22:19
	'	00011021	US-PGPUB;	2003/04/17 22.13
			EPO; JPO;	
			DERWENT:	
ľ				
_	24	(((motherboard mother-board (mother board) PCB (main adj1	IBM_TDB USPAT;	2003/04/17 22:21
ļ -	24	circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	2003/04/1/22.21
		microprocessor processor) and dsp and memory and		
		((spoken speech verbal voice audio) adj1 command)) and	EPO; JPO;	
			DERWENT;	
	50	704/\$.ccls.) and bus and (memory same dsp)	IBM_TDB	2002/04/40 40:44
-	50	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 10:41
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	
		pci)) and pci	IBM_TDB	0000/04/40 40 40
-	34	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 10:42
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	
		pci)) and pci and speech	IBM_TDB	
-	29	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:06
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	
		pci)) and pci and (speech adj1 recognit\$4)	IBM_TDB	
-	20	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 15:57
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
1		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	
		pci)) and pci and (speech adj1 recognit\$4) and power	IBM_TDB	
-	19	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:05
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
1		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	
		pci)) and pci and (speech adj1 recognit\$4) and power) and	IBM_TDB	
		dynamic\$5 and command		
-	1	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:00
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	
		pci)) and pci and (speech adj1 recognit\$4) and power) and	IBM_TDB	
		dynamic\$5 and command and phoneme		
-	50	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:15
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.	IBM_TDB	

-	42	(((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu	USPAT; US-PGPUB;	2003/04/18 16:17
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1	IBM_TDB	
		recognition)		
-	29	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:15
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
i I		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and (bus	DERWENT;	
		pci)) and pci and (speech adj1 recogni\$4)	IBM_TDB	
-	32	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:18
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1	IBM_TDB	
	11	recognition) and bus	LICDAT.	2002/04/40 40:40
-	11	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 18:19
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus and phoneme and (continuous adj1	IBM_TDB	
	<u> </u>	speech) and (voice adj1 command)		
_	20	(((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 20:50
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	2000/07/10 20:00
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1	IBM TDB	
		recognition) and bus and phoneme		
-	6	"06154830" "06085314"	USPAT:	2003/04/18 18:24
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	2	"06170049"	USPAT;	2003/04/18 18:24
			US-PGPUB;	
ı			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	20	(((motherboard mother-board (mother board) PCB) and	USPAT;	2003/04/18 20:53
		computer and (cpu microprocessor processor) and dsp and	US-PGPUB;	
		memory and ((spoken speech verbal voice audio) adj1	EPO; JPO;	
ı		command)) and 704/\$.ccls.) and vocabulary and ((speech	DERWENT;	
	10	voice) adj1 recognition) and bus and phoneme	IBM_TDB	2002/04/49 20:54
-	10	(((motherboard mother-board) and computer and (cpu	USPAT;	2003/04/18 20:51
		microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and	US-PGPUB; EPO; JPO;	
ı		704/\$.ccls.) and vocabulary and ((speech voice) adj1	DERWENT;	
ı		recognition) and bus and phoneme	IBM TDB	
_	20	((((mother board) PCB) and computer and (cpu	USPAT;	2003/04/18 20:53
ı	20	microprocessor processor) and dsp and memory and	US-PGPUB;	2000/07/10 20:00
		((spoken speech verbal voice audio) adj1 command)) and	EPO; JPO;	
				1
		704/\$.ccls.) and vocabulary and ((speech voice) adi1	DERWENT	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1	DERWENT;	
-	11	recognition) and bus and phoneme	IBM_TDB	2003/04/18 20:55
-	11	recognition) and bus and phoneme ((motherboard mother-board (mother adj1 board) PCB (main	IBM_TDB USPAT;	2003/04/18 20:55
· -	11	recognition) and bus and phoneme ((motherboard mother-board (mother adj1 board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu	IBM_TDB USPAT; US-PGPUB;	2003/04/18 20:55
-	11	recognition) and bus and phoneme ((motherboard mother-board (mother adj1 board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/04/18 20:55
-	11	recognition) and bus and phoneme ((motherboard mother-board (mother adj1 board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/04/18 20:55
-	11	recognition) and bus and phoneme ((motherboard mother-board (mother adj1 board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci and (memory same dsp) and power	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	
-		recognition) and bus and phoneme ((motherboard mother-board (mother adj1 board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/18 20:55
-		recognition) and bus and phoneme ((motherboard mother-board (mother adj1 board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci and (memory same dsp) and power	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;	
- -	87	recognition) and bus and phoneme ((motherboard mother-board (mother adj1 board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci and (memory same dsp) and power PCI and PDA and dsp and cpu	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB	2003/04/18 21:28
-	87	recognition) and bus and phoneme ((motherboard mother-board (mother adj1 board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci and (memory same dsp) and power PCI and PDA and dsp and cpu PCI and PDA and dsp and cpu and ((speech voice) adj1	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB USPAT;	2003/04/18 21:28

-	3	PCI and ((cellular mobile) adj1 phone) and vocabulary	USPAT;	2003/04/18 22:56
			US-PGPUB	
-	46	704/\$.ccls. and ((mobile (phone telephone)) or (cellular	USPAT;	2003/04/19 20:11
		phone)) and DSP and (noise adj1 cancel\$6)	US-PGPUB;	
			EPO; JPO;	,
			DERWENT;	
			IBM_TDB	
-	4	(dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice	USPAT;	2003/04/19 21:03
		adj1 dialing)	US-PGPUB	
-	7	(dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice	USPAT;	2003/04/19 21:05
		adj1 dialing)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	(menu near1 select\$) and (voice adj1 dialing) and (noise adj1	USPAT;	2003/04/19 21:29
		cancellation)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	•
-	5	(voice adj1 dialing) and (dsp (dsp adj1 chip)) and (menu adj1	USPAT;	2003/04/19 22:54
		select\$)	US-PGPUB;	
		·	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	5	(noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu	USPAT;	2003/04/19 23:05
		adj1 select\$)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB]
-	13	(noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu)	USPAT;	2003/04/19 23:10
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	20	(noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech	USPAT;	2003/04/19 23:14
		adj1 recognition)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	5	((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech	USPAT;	2003/04/19 23:13
		adj1 recognition)) and (dsp same (speech adj1 recognition))	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	(noise adj1 cancel\$5) same (dsp) and (speech adj1	USPAT;	2003/04/19 23:14
		recognition)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	İ		IBM TDB	